

IN THE CLAIMS

Please cancel claims 1-17 and add claims 18-19, as indicated below:

18. An arbitration circuit for a computer system, the arbitration circuit adapted to couple with a plurality of slave devices having transactions queued for execution, the arbitration circuit further adapted to signal any the slave devices to reorder their transactions without signaling a microprocessor of the computer system.

19. A computer-implemented method for reordering transactions, the method comprising:
receiving and queuing within a slave device a plurality of transactions for execution;
signaling the slave device to reorder its transactions without signaling a
microprocessor of the computer system that the transactions are being
reordered.

STATUS OF CLAIMS AND SUPPORT FOR CLAIM CHANGES


Original claims 1-17 are canceled, and new claims 18-19 are pending. Support for new claims 18-19 can be found in the specification of the issued patent at cols. 9-11 *et seq.* and cols. 18-19 *et seq.*

Applicant is claiming priority under 35 U.S.C. 120, from U.S. Patent Application No. 08/432,622, filed May 2, 1995, now abandoned. Applicant is also adding Michael L. Regal as an inventor under 37 C.F.R. 1.175.

Respectfully submitted,

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